

	Type	L #	Hits	Search Text	DBs
5	IS&R	L9	838	(363/59-60) .CCLS.	US-PGPUB; USPAT
6	BRS	L10	667	9 not 1	US-PGPUB; USPAT
7	BRS	L11	420	10 and transistor\$1	US-PGPUB; USPAT
8	BRS	L12	18327	6 not 7	USOCR; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L13	17725	12 not (lock\$2 adj loop DLL PLL)	USOCR; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L14	1065	13 and transistor	USOCR; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L15	657	13 and clock\$1	USOCR; EPO; JPO; DERWENT; IBM_TDB
12	IS&R	L18	1073	(365/189.11) .CCLS.	US-PGPUB; USPAT
13	BRS	L19	480	18 and (charge adj pump boost\$3)	US-PGPUB; USPAT
14	BRS	L20	333	19 and capacitor\$1 and transistor\$1	US-PGPUB; USPAT
15	IS&R	L21	1850	(365/226) .CCLS.	US-PGPUB; USPAT
16	BRS	L22	727	21 and (charge adj pump boost\$3)	US-PGPUB; USPAT

	Type	L #	Hits	Search Text	DBs
17	BRS	L23	566	22 and capacitor\$1 and transistor\$1	US-PGPUB; USPAT
18	BRS	L24	465	23 not 20	US-PGPUB; USPAT
19	IS&R	L25	307	(327/589).CCLS.	US-PGPUB; USPAT
20	BRS	L26	233	25 not 1	US-PGPUB; USPAT